ABSTRACT OF THE DISCLOSURE

In a moving image display mode, CPU commands a selector to input an output signal of a frequency divider to a signal processing part as a synchronous signal. In case of a CCD with G-stripe or Bayer arrangement, the frequency divider divides the frequency of a clock by an odd number (for example, 3 or 5) and the frequency-divided clock is inputted to the signal processing part. This thins out the pixels of image data to {1/(an odd number)} and the image data is inputted to the signal processing part without changing the arrangement of the pixels. The inputted image data is processed at the signal processing part, and then is outputted to the liquid crystal monitor, which displays a moving image.